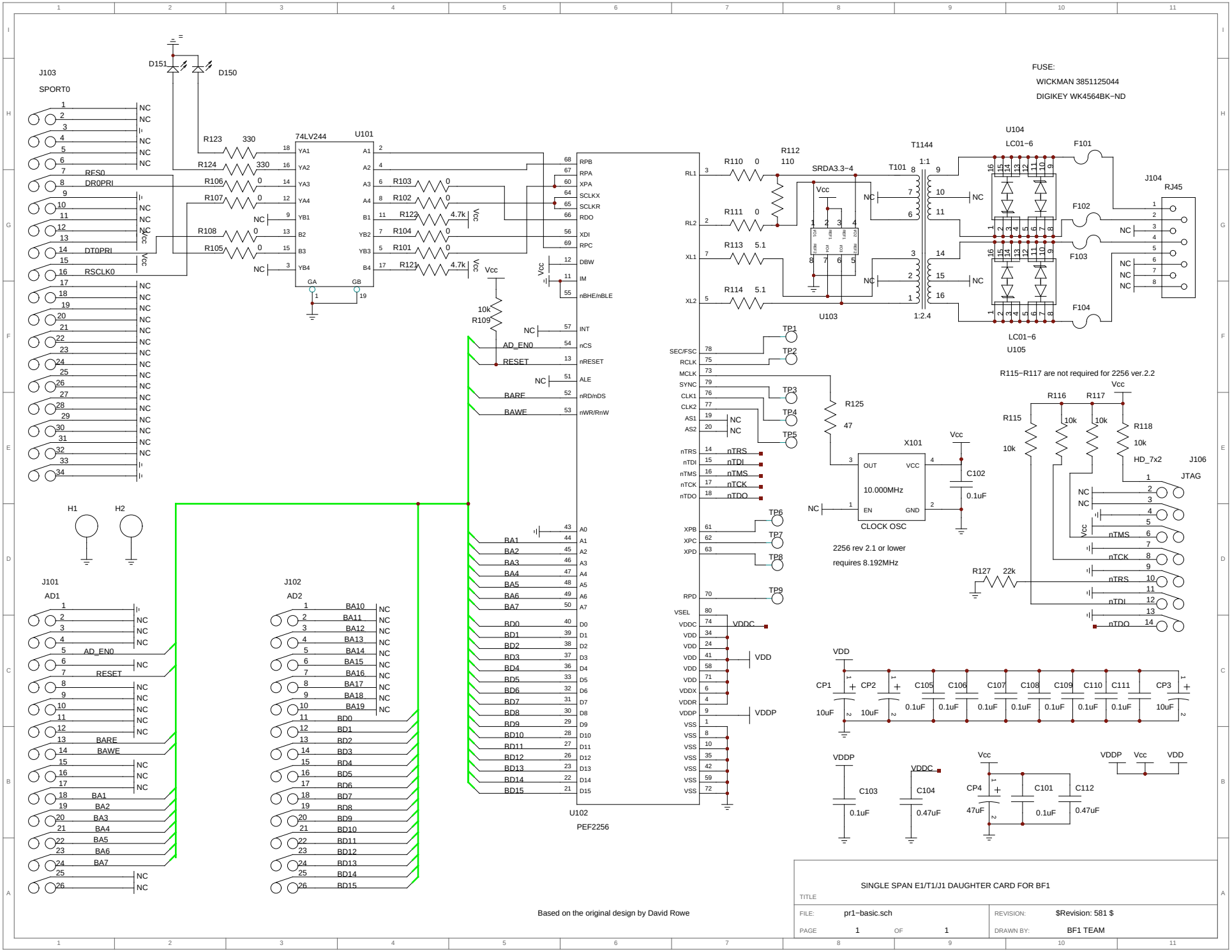
J103
SPORT0
D151
D150
RFS0
DR0PRI
DT0PRI
RSCLK0
R123 330
R124 330
R106 0
R107 0
R108 0
R105 0
74LV244
U101
R103 0
R102 0
R122 4.7k
R104 0
R101 0
R121 4.7k
Vcc
10k
R109
AD_EN0
RESET
BARE
BAWE
RPB
RPA
XPA
SCLKX
SCLKR
RDO
XDI
RPC
DBW
IM
nBHE/nBLE
INT
nCS
nRESET
ALE
nRD/nDS
nWR/RnW
A0
BA1
BA2
BA3
BA4
BA5
BA6
BA7
BD0
BD1
BD2
BD3
BD4
BD5
BD6
BD7
BD8
BD9
BD10
BD11
BD12
BD13
BD14
BD15
U102
PEF2256
H1
H2
J101
AD1
J102
AD2
RL1
RL2
XL1
XL2
R110 0
R111 0
R113 5.1
R114 5.1
R112 110
SRDA3.3−4
U103
T1144
T101
1:1
1:2.4
U104
LC01−6
U105
F101
F102
F103
F104
J104
RJ45
SEC/FSC
RCLK
MCLK
SYNC
CLK1
CLK2
AS1
AS2
nTRS
nTDI
nTMS
nTCK
nTDO
XPB
XPC
XPD
RPD
VSEL
VDDC
VDD
VDDX
VDDR
VDDP
VSS
TP1
TP2
TP3
TP4
TP5
TP6
TP7
TP8
TP9
R125 47
X101
OUT
VCC
EN
GND
10.000MHz
CLOCK OSC
C102 0.1uF
2256 rev 2.1 or lower
requires 8.192MHz
R115−R117 are not required for 2256 ver.2.2
R116
R117
R115 10k
R118 10k
HD_7x2
J106
JTAG
R127 22k
CP1 10uF
CP2 10uF
C105 0.1uF
C106 0.1uF
C107 0.1uF
C108 0.1uF
C109 0.1uF
C110 0.1uF
C111 0.1uF
CP3 10uF
C103 0.1uF
C104 0.47uF
CP4 47uF
C101 0.1uF
C112 0.47uF
FUSE:
WICKMAN 3851125044
DIGIKEY WK4564BK−ND
Based on the original design by David Rowe
TITLE
SINGLE SPAN E1/T1/J1 DAUGHTER CARD FOR BF1
FILE: pr1−basic.sch
REVISION: $Revision: 581 $
PAGE 1 OF 1
DRAWN BY: BF1 TEAM